



AMENDMENT TRANSMITTAL LETTER

Docket No.
M4065.0340/P340

Application No.
09/652,003-Conf. #2935

Filing Date
August 31, 2000

Examiner
W. H. Choi

Art Unit
2186

Applicant(s): Graham Kirsch

Invention: METHOD AND APPARATUS FOR CONNECTING A MASSIVELY PARALLEL PROCESSOR ARRAY TO A MEMORY ARRAY IN A BIT SERIAL MANNER

TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.


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CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	43	- 48 =		x	0.00
Independent Claims	15	- 15 =		x	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					0.00

- ☒ Large Entity ☐ Small Entity
- ☒ No additional fee is required for this amendment.
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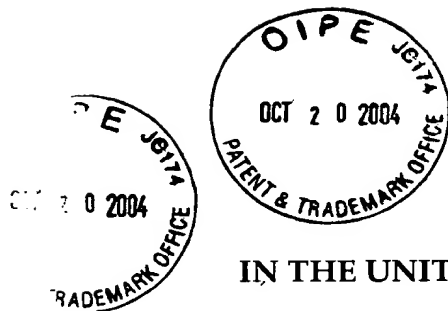

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Dated: October 21, 2004

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In re Patent Application of:
Graham Kirsch

Application No.: 09/652,003

Art Unit: 2186

Filed: August 31, 2000

Examiner: W. H. Choi

For: METHOD AND APPARATUS FOR
CONNECTING A MASSIVELY PARALLEL
PROCESSOR ARRAY TO A MEMORY
ARRAY IN A BIT SERIAL MANNER

AMENDMENT AFTER FINAL ACTION (37 C.F.R. SECTION 1.116)

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Office Action dated July 22, 2004 (Paper No. 19), finally rejecting claims 1-6, 11-13, 22-27, 33-35, 41, 43-49 and 51, please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 34 of this paper.